UTILITY A PATENT APPLICATION TRANSMITTAL (Only for new nonprovisional applications under 37 CFR 1 53(b))

Attorney Docket No. 216648US2

First Inventor or Application Identifier Hiroyoshi TANIMOTO, et al.

SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, AND SYSTEM FOR EVALUATING ELECTRICAL CHARACTERISTICS OF SEMICONDUCTOR DEVICE

Assignee Name: KABUSHIKI KAISHA TOSHIBA

	1	Assignee Address: 1-1	, Shibau	ıra 1-ch	ome, Minato-ku, Tokyo, Ja	apan	75	
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	APPLICATION ELEMENT See MPEP chapter 600 concerning utility patent a M	S pplication contents	ADDRE	ESS TO:	Assistant Commission Box Patent Application Washington, DC 2023	er for Patents 1 1	1,1	
	Fee Transmittal Form (e.g. PTO/SB/ (Submit an original and a duplicate for fee pro	17) cessing)	7		OMPANYING APPLICAT		-11	
2.		Sheets 35	Į.	Applic	ment Papers (cover shee ation Data Sheet. See 37 F.R. §3.73(b) Statement here is an assignee)	`		
3.	Formal Drawing(s) (35 U.S.C. 113) Total	Sheets 8	10. 🗆	•	h Translation Document <i>(</i> ation Disclosure	if applicable) Copies of	IDS	
4. 5. 6. mig with first first first first first first first first	□ Oath or Declaration Total a. □ Newly executed (original or cop b. □ Copy from a prior application (3 (for continuation / divisional w/ box i. □ DELETION OF INVENTO Signed statement attached delet the prior application, see 37 C.F. 1 33(b). □ CD-ROM or CD-R in duplicate, large Program (Appendix) □ Nucleotide and/or Amino Acid Sequ (if applicable, all necessary) a. □ Computer Readable Form (CRI b. Specification or Sequence Listing or i. □ CD-ROM or CD-R (2 copi ii. □ Paper	7 C.F.R, §1.63(d)) 17 completed) R(S) ng inventor(s) named in R. §1.63(d)(2) and e table or Computer ence Submission	14. 🗆 15. 🗀	Staten Prelim White Certific (if foreign	nent (IDS)/PTO-1449 inary Amendment Advance Serial No. Posto ed Copy of Priority Docum in priority is claimed) ant claims small entity sta CFR 1.27	Citations (card nent(s)	2)	
4 7.	c. Statements verifying identity of If a CONTINUING APPLICATION, check Continuation Divisional Prior application information: Examine CONTINUATION OR DIVISIONAL APPS only: The idered a part of the accompanying continuation or or a portion has been inadvertently omitted from the	appropriate box, and support Continuation	in-part	(CIP)	of prior application no.: Group Art Unit:		4b, is ied upon	
18. Amend the specification by inserting before the first line the sentence: ☐ This application is a ☐ Continuation ☐ Division ☐ Continuation-in-part (CIP) of application Serial No. Filed on ☐ Which was published in English ☐ Which was not published in English ☐ This application claims priority of provisional application Serial No. Filed								
		19. CORRESPOND	ENCE A	ADDRES	SS			

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216648US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hiroyoshi TANIMOTO, et al.

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

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SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, AND SYSTEM FOR EVALUATING ELECTRICAL CHARACTERISTICS OF SEMICONDUCTOR DEVICE

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS		
TOTAL CLAIMS	22 - 20 =	2	× \$18 =	\$36.00		
INDEPENDENT CLAIMS	8 - 3 =	5	× \$84 =	\$420.00		
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Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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